## C.U.SHAH UNIVERSITY Summer Examination-2017

## **Subject Name : Electronics**

	Subject (	Code:4	SC04PHE1	Branch : B.Sc. (A	<b>\II</b> )			
	Semester		Date : 28/04/2017	Time : 10:30 To 0	1:30 Marks : 70	)		
	Instructio							
	• •	(1) Use of Programmable calculator & any other electronic instrument is prohibited.						
		(2) Instructions written on main answer book are strictly to be obeyed.						
				s (if necessary) at right pl	aces.			
	(4) A	Assume s	suitable data if needed					
		• • •				(4.4)		
Q-1		_	ot the following quest	ions:		(14)		
			o you mean by BJT?			01		
		-	uish BJT and UJT.	,		01		
	<b>c</b> )		aving termin	als.		01		
			dvantages of UJT.			01		
			sefulness of JFET.			01		
	<b>f</b> )		chematic diagram of N			01		
	<b>g</b> )			hy it becomes necessary		01		
	· · · ·		•	nd useful in digital electro	onics?	01		
	i)		stability factor.	1		01		
	-		t 20.78125 <sub>10</sub> into bina	•		01		
			te $110011_2$ into decim			01		
	l)		universal logic gates.			01		
			s thermistor?	1 1		01		
		-	uish analog and digita			01		
		-	stions from Q-2 to Q	-8				
Q-2		_	ot all questions			(14)		
	a)			sistor biasing can be don	he through feedback resistor	r 07		
	1 \	0	method.	· · · · · · · · · · · · · · · · · · ·	·/ 11 ·/	07		
	b)	Discuss	s briefly base resistor l	biasing method. Write its	merits and demerits.	07		
Q-3		Attem	ot all questions			(14)		
C				construction and workin	g principle.	07		
	<b>b</b> )	-	-	s in transistor amplifier?		07		
Q-4		Attemp	ot all questions			(14)		
-	<b>a</b> )		uish JFET and BJT br	iefly.		07		
	<b>b</b> )	What is	s MOSFET? Explain v	vorking of MOSFET.		07		

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Q-5		Attempt all questions	
	a)	How UJT works as a relaxation oscillator? Write its advantages.	07
	b)	How transistor amplifier works practically? – Explain.	07
Q-6		Attempt all questions	(14)
	a)	Discuss JFET parameters briefly.	07
	b)	What is the physical significance of load line in transistor amplifier? Discuss its analysis.	07
Q-7		Attempt all questions	(14)
-	a)	Write short notes on (1) OR gate and (2) NOT gate.	07
	b)	Discuss how AND, OR and NOT gate can be made through NAND gate.	07
Q-8		Attempt all questions	(14)
-	a)	For a given JFET, if a change in drain voltage of 4V produces a change in drain current of 0.06 mA. Calculate ac drain resistance.	04
	b)	Discuss-Thermistor characteristics.	05
	(	Write short note on AND gate.	05
	<b>c</b> )	while short hole on AND gale.	05

